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SERIAL NUMBER (Series of 1987)	PATENT DATE		PATENT NUMBER	
07/458507				

SERIAL NUMBER	FILING DATE	CLASS	SUBCLASS	GROUP PART UNIT	EXAMINER
07/458,507	12/28/89 RULE 60	324	158	267536	Baker

APPLICANTS

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**\*\*CONTINUING DATA\*\*\*\*\***  
 VERIFIED THIS APPLN IS A DIV OF 07/140,628 01/04/88 *Baker 324/158 Filed #4916,389*  
 WHICH IS A CON OF 06/562,969 12/19/83 *abandoned*  
*SMB*

**\*\*FOREIGN/PCT APPLICATIONS\*\*\*\*\***  
 VERIFIED JAPAN 220083/82 12/17/82 ✓  
*SMB*

Foreign priority claimed 35 USC 119 conditions met	Yes <input checked="" type="checkbox"/> No <input type="checkbox"/>	AS FILED	STATE OR COUNTRY	SHEETS OR DRWS.	TOTAL CLAIMS	INDEP. CLAIMS	FILING FEE RECEIVED	ATTORNEY'S DOCKET NO.
Verified and Acknowledged	<i>SMB</i>	→	JPX	16	1	1	\$ 370.00	50120699VC1

ADDRESS

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TITLE

SEMICONDUCTOR INTEGRATED CIRCUIT *With voltage limiter having different output ranges for normal operation and performing of aging tests.*  
*166*

U.S. DEPT. of COMM.-Pat. & TM Office - PTO-436L (rev. 10-78)

PARTS OF APPLICATION  
FILED SEPARATELY

NOTICE OF ALLOWANCE MAILED		PREPARED FOR ISSUE		CLAIMS ALLOWED	
		Assistant Examiner	Docket Clerk	Total Claims	Print Claim
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.
Label Area		ISSUE CLASSIFICATION		ISSUE BATCH NUMBER	
		Class	Subclass		
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